



Attorney Docket No. 1075.1249

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Keisuke MURAYA, et al.

Application No.: 10/780,610 ✓

Confirmation No.: 3175

Filed: February 19, 2004

U.S. Patent No.: 6,847,222

Issued: January 25, 2005

ATTENTION

CERTIFICATE OF CORRECTION

BRANCH

Batch No.:

Certificate  
JUN 17 2005  
of Correction

For: APPARATUS FOR MEASURING VOLTAGE FLUCTUATION WAVEFORM IN  
SEMICONDUCTOR INTEGRATED CIRCUIT, AND SEMICONDUCTOR INTEGRATED  
CIRCUIT HAVING FUNCTION FOR MEASURING VOLTAGE FLUCTUATION  
WAVEFORM

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Patentees respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistakes shown on the attached Form PTO-1050.

Since the mistakes are those of the Applicant, a check in the amount of \$100.00 is enclosed herewith.

06/15/2005 JBALINAN 00000058 6847222

01 FC:1811

100.00 DP

Respectfully submitted,

STAAS & HALSEY LLP

Date:

June 14, 2005

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 6,847,222

DATED : January 25, 2005

INVENTOR(S) : Keisuke MURAYA, et al.

(57) Abstract

Line 4, change "propose" to --purpose--.

Col. 17, line 23, after "and", insert a new paragraph.

MAILING ADDRESS OF SENDER:

STAAS & HALSEY LLP  
1201 New York Ave., N.W.  
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PATENT NO. 6,847,222

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